WAGO-I/O-SYSTEM 750

Manual



750-637(/xxx-xxx) Incremental Encoder Interfaces

Version 1.6.0



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Every conceivable measure has been taken to ensure the accuracy and completeness of this documentation. However, as errors can never be fully excluded, we always appreciate any information or suggestions for improving the documentation.

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We wish to point out that the software and hardware terms as well as the trademarks of companies used and/or mentioned in the present manual are generally protected by trademark or patent.



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1 Notes about this Documentation



Note

Always retain this documentation!

This documentation is part of the product. Therefore, retain the documentation during the entire service life of the product. Pass on the documentation to any subsequent user. In addition, ensure that any supplement to this documentation is included, if necessary.

1.1 Validity of this Documentation

This documentation is only applicable to the I/O module 750-637 (Incremental Encoder Interfaces) and the variants listed in the table below.

Tabelle 1: Varianten

| Bestellnummer/Variante | Bezeichnung | |
|------------------------|---------------------------------------------------------------------|--|
| 750-637 | Incremental Encoder Interfaces | |
| 750-637/000-001 | Incremental Encoder Interfaces 24 V/32 Bit differential | |
| 750-637/000-002 | Incremental Encoder Interfaces 24 V/32 Bit single ended | |
| 750-637/000-003 | Incremental Encoder Interfaces RS-422/32Bit/Single Interpreter | |
| 750-637/000-004 | Incremental Encoder Interfaces 24 V/32 Bit single ended/cam outputs | |



Note

Documentation Validity for Variants

Unless otherwise indicated, the information given in this documentation applies to listed variants.

The I/O module 750-637 shall only be installed and operated according to the instructions in this manual and in the manual for the used fieldbus coupler/controller.

NOTICE

Consider power layout of the WAGO-I/O-SYSTEM 750!

In addition to these operating instructions, you will also need the manual for the used fieldbus coupler/controller, which can be downloaded at www.wago.com. There, you can obtain important information including information on electrical isolation, system power and supply specifications.



1.2 Copyright

This Manual, including all figures and illustrations, is copyright-protected. Any further use of this Manual by third parties that violate pertinent copyright provisions is prohibited. Reproduction, translation, electronic and phototechnical filing/archiving (e.g., photocopying) as well as any amendments require the written consent of WAGO Kontakttechnik GmbH & Co. KG, Minden, Germany. Non-observance will involve the right to assert damage claims.



1.3 Symbols

DANGER

Personal Injury!

Indicates a high-risk, imminently hazardous situation which, if not avoided, will result in death or serious injury.



▲ DANGER

Personal Injury Caused by Electric Current!

Indicates a high-risk, imminently hazardous situation which, if not avoided, will result in death or serious injury.

WARNING

Personal Injury!

Indicates a moderate-risk, potentially hazardous situation which, if not avoided, could result in death or serious injury.

△ CAUTION

Personal Injury!

Indicates a low-risk, potentially hazardous situation which, if not avoided, may result in minor or moderate injury.

NOTICE

Damage to Property!

Indicates a potentially hazardous situation which, if not avoided, may result in damage to property.



NOTICE

Damage to Property Caused by Electrostatic Discharge (ESD)!

Indicates a potentially hazardous situation which, if not avoided, may result in damage to property.



Note

Important Note!

Indicates a potential malfunction which, if not avoided, however, will not result in damage to property.





Information

Additional Information:

Refers to additional information which is not an integral part of this documentation (e.g., the Internet).



1.4 Number Notation

Table 2: Number Notation

| Number Code | Example | Note |
|-------------|-------------|-------------------------------------------|
| Decimal | 100 | Normal notation |
| Hexadecimal | 0x64 | C notation |
| Binary | '100' | In quotation marks, nibble separated with |
| | '0110.0100' | dots (.) |

1.5 Font Conventions

Table 3: Font Conventions

| Font Type | Indicates | | |
|------------------|------------------------------------------------------------------------|--|--|
| italic | Names of paths and data files are marked in italic-type. | | |
| | e.g.: C:\Program Files\WAGO Software | | |
| Menu | Menu items are marked in bold letters. | | |
| | e.g.: Save | | |
| > | A greater-than sign between two names means the selection of a | | |
| | menu item from a menu. | | |
| | e.g.: File > New | | |
| Input | Designation of input or optional fields are marked in bold letters, | | |
| | e.g.: Start of measurement range | | |
| "Value" | Input or selective values are marked in inverted commas. | | |
| | e.g.: Enter the value "4 mA" under Start of measurement range . | | |
| [Button] | Pushbuttons in dialog boxes are marked with bold letters in square | | |
| | brackets. | | |
| | e.g.: [Input] | | |
| [Key] | Keys are marked with bold letters in square brackets. | | |
| | e.g.: [F5] | | |



2 Important Notes

This section includes an overall summary of the most important safety requirements and notes that are mentioned in each individual section. To protect your health and prevent damage to devices as well, it is imperative to read and carefully follow the safety guidelines.

2.1 Legal Bases

2.1.1 Subject to Changes

WAGO Kontakttechnik GmbH & Co. KG reserves the right to provide for any alterations or modifications that serve to increase the efficiency of technical progress. WAGO Kontakttechnik GmbH & Co. KG owns all rights arising from the granting of patents or from the legal protection of utility patents. Third-party products are always mentioned without any reference to patent rights. Thus, the existence of such rights cannot be excluded.

2.1.2 Personnel Qualifications

All sequences implemented on WAGO-I/O-SYSTEM 750 devices may only be carried out by electrical specialists with sufficient knowledge in automation. The specialists must be familiar with the current norms and guidelines for the devices and automated environments.

All changes to the coupler or controller should always be carried out by qualified personnel with sufficient skills in PLC programming.

2.1.3 Use of the WAGO-I/O-SYSTEM 750 in Compliance with Underlying Provisions

Fieldbus couplers, fieldbus controllers and I/O modules found in the modular WAGO-I/O-SYSTEM 750 receive digital and analog signals from sensors and transmit them to actuators or higher-level control systems. Using programmable controllers, the signals can also be (pre-) processed.

The devices have been developed for use in an environment that meets the IP20 protection class criteria. Protection against finger injury and solid impurities up to 12.5 mm diameter is assured; protection against water damage is not ensured. Unless otherwise specified, operation of the devices in wet and dusty environments is prohibited.

Operating the WAGO-I/O-SYSTEM 750 devices in home applications without further measures is only permitted if they meet the emission limits (emissions of interference) according to EN 61000-6-3. You will find the relevant information in the section "Device Description" > "Standards and Guidelines" in the manual for the used fieldbus coupler/controller.



Appropriate housing (per 94/9/EG) is required when operating the WAGO-I/O-SYSTEM 750 in hazardous environments. Please note that a prototype test certificate must be obtained that confirms the correct installation of the system in a housing or switch cabinet.

2.1.4 Technical Condition of Specified Devices

The devices to be supplied ex works are equipped with hardware and software configurations, which meet the individual application requirements. WAGO Kontakttechnik GmbH & Co. KG will be exempted from any liability in case of changes in hardware or software as well as to non-compliant usage of devices.

Please send your request for modified and new hardware or software configurations directly to WAGO Kontakttechnik GmbH & Co. KG.



2.2 Safety Advice (Precautions)

For installing and operating purposes of the relevant device to your system the following safety precautions shall be observed:



DANGER

Do not work on devices while energized!

All power sources to the device shall be switched off prior to performing any installation, repair or maintenance work.

A DANGER

Install the device only in appropriate housings, cabinets or in electrical operation rooms!

The WAGO-I/O-SYSTEM 750 and its components are an open system. As such, install the system and its components exclusively in appropriate housings, cabinets or in electrical operation rooms. Allow access to such equipment and fixtures to authorized, qualified staff only by means of specific keys or tools.

NOTICE

Replace defective or damaged devices!

Replace defective or damaged device/module (e.g., in the event of deformed contacts), since the long-term functionality of device/module involved can no longer be ensured.

NOTICE

Protect the components against materials having seeping and insulating properties!

The components are not resistant to materials having seeping and insulating properties such as: aerosols, silicones and triglycerides (found in some hand creams). If you cannot exclude that such materials will appear in the component environment, then install the components in an enclosure being resistant to the above-mentioned materials. Clean tools and materials are imperative for handling devices/modules.

NOTICE

Clean only with permitted materials!

Clean soiled contacts using oil-free compressed air or with ethyl alcohol and leather cloths.



NOTICE

Do not use any contact spray!

Do not use any contact spray. The spray may impair contact area functionality in connection with contamination.

NOTICE

Do not reverse the polarity of connection lines!

Avoid reverse polarity of data and power supply lines, as this may damage the devices involved.



NOTICE

Avoid electrostatic discharge!

The devices are equipped with electronic components that may be destroyed by electrostatic discharge when touched. Please observe the safety precautions against electrostatic discharge per DIN EN 61340-5-1/-3. When handling the devices, please ensure that environmental factors (personnel, work space and packaging) are properly grounded.



3 Device Description

This module is an interface for different versions of incremental encoders with various connections (RS-422, differential, single-ended).

The data width for the encoder module is 32 bits. Either the current counter value, the latch value, the set value or the current rate can be shown in the process data.

The table below provides an overview of the different versions and their connections:

Table 4: Inputs/Outputs - Versions 750-637 and 750-637/000-003

| Inputs/Outputs | | |
|----------------------------------------------|-----------------------------------|--|
| A, /A, B, /B Quadrature inputs, 24 V, RS-422 | | |
| C, /C | Initial point input, 24 V, RS-422 | |
| Latch, Gate, Ref | Input, 24 V | |
| N1, N2 | Output, 24 V | |

Table 5: Inputs/Outputs - Versions 750-637/000-001

| Inputs/Outputs | | |
|----------------------------------------------------|-----------------------------------------|--|
| A, /A, B, /B Quadrature inputs, 24 V, differential | | |
| C, /C | Initial point input, 24 V, differential | |
| Latch, Gate, Ref | Input, 24 V | |
| N1, N2 | Output, 24 V | |

Table 6: Inputs/Outputs - Versions 750-637/000-002 and 750-637/000-004

| Inputs/Outputs | | |
|--------------------------------------------|-----------------------------------------|--|
| A, B Quadrature inputs, 24 V, single-ended | | |
| С | Initial point input, 24 V, single-ended | |
| Latch, Gate, Ref | Input, 24 V | |
| N1, N2 | Output, 24 V | |

A counter with quadrature decoder as well as a latch for the zero impulse can be read or enabled by the control. The control can set the counter. Depending on the operating mode, the counter value is added to the latch register on a positive edge at the "C" or "Latch" input.

The rate (increments/ms) is automatically acquired and can also be transmitted to the control as an alternative to the latch value.

A counter lock-out is possible using the "Gate" input. Input "Ref" can be used to activate the initial point "C" function.

The cam outputs N1 and N2 indicate whether the counter value is within a defined range of values. The range can be adjusted for each cam via the control unit.



Starting with the software version XXXX03XX, the cam outputs can be disabled by the control unit and the states of the cam outputs can be queried.

The signal status for inputs A, B and C, Latch and Gate and the operating status of the I/O module are each indicated by a dedicated green status LED.

The meaning of the LEDs is described in the "Display Elements" section.

Power supply for 24 V encoders is provided via the 24 V field supply connections. The encoder ground must be connected with the 0 V module.

Power supply for 5 V encoders can be tapped directly at connections V_e and V_0 of the standard module 750-637.



Note

Use a supply module!

Use a supply module for field-side power supply of downstream I/O modules.

The field voltage and the system voltage are electrically isolated from each other.

The I/O module 750-637 can be used with all fieldbus couplers/controllers of the WAGO-I/O-SYSTEM 750 (except for the economy types 750-320, -323, -324 and -327).



3.1 View

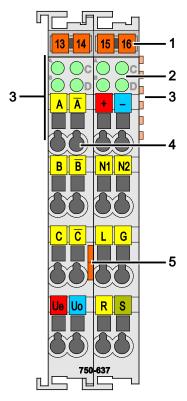


Figure 1: View

Table 7: Legend for Figure "View"

| Pos. | Description | Details See Section |
|------|-----------------------------------|-------------------------------------------|
| 1 | Marking possibility with Mini-WSB | |
| 2 | Status LEDs | "Device Description" > "Display Elements" |
| 3 | Data contacts | "Device Description" > "Connectors" |
| 4 | CAGE CLAMP® connectors | "Device Description" > "Connectors" |
| 5 | Release tab | "Mounting" > "Inserting and Removing |
| | | Devices" |



3.2 Connectors

3.2.1 Data Contacts/Internal Bus

Communication between the fieldbus coupler/controller and the I/O modules as well as the system supply of the I/O modules is carried out via the internal bus. It is comprised of 6 data contacts, which are available as self-cleaning gold spring contacts.

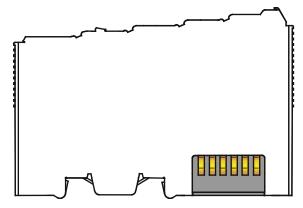


Figure 2: Data Contacts

NOTICE

Do not place the I/O modules on the gold spring contacts!

Do not place the I/O modules on the gold spring contacts in order to avoid soiling or scratching!



NOTICE

Ensure that the environment is well grounded!

The devices are equipped with electronic components that may be destroyed by electrostatic discharge. When handling the devices, ensure that the environment (persons, workplace and packing) is well grounded. Avoid touching conductive components, e.g. data contacts.



3.2.2 Power Jumper Contacts/Field Supply

The I/O module 750-637 has no power jumper contacts.



3.2.3 CAGE CLAMP® Connectors

3.2.3.1 Versions 750-637 and 750-637/000-003

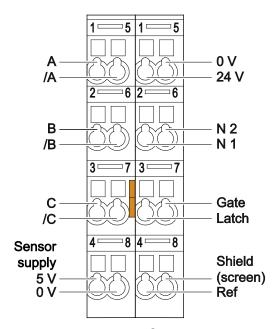


Figure 3: CAGE CLAMP® Connectors – Versions 750-637 and 750-637/000-003

Table 8: Legend for Figure "CAGE CLAMP® Connectors – Versions 750-637 and 750-637/000-003"

| Designation | Connector | Function |
|-----------------|-----------|----------------------------------|
| A | 1, left | |
| В | 2, left | |
| С | 3, left | Connection of songer |
| /A | 5, left | Connection of sensor |
| /B | 6, left | |
| /C | 7, left | |
| 5 V | 4, left | C |
| 0 V | 8, left | Sensor supply |
| 24 V | 1, right | Cumply valtage input |
| 0 V | 5, right | Supply voltage input |
| N1 | 2, right | Compositorita |
| N2 | 6, right | Cam outputs |
| Latch | 3, right | Latch input |
| Gate | 7, right | Gate input |
| Ref | 4, right | Ref input |
| Shield (screen) | 8, right | Shield connection (carrier rail) |



3.2.3.2 Version 750-637/000-001

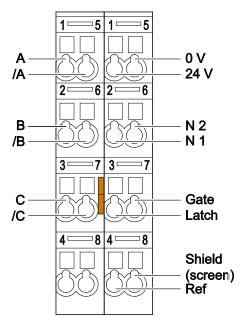


Figure 4: CAGE CLAMP® Connectors – Version 750-637/000-001

Table 9: Legend for "CAGE CLAMP® Connectors – Version 750-637/000-001" Figure

| Designation | Connection | Function |
|-------------|------------|------------------------------|
| A | 1, left | |
| В | 2, left | |
| C | 3, left | Samon compaction |
| /A | 5, left | Sensor connection |
| /B | 6, left | |
| /C | 7, left | |
| - | 4, left | Not assigned |
| - | 8, left | Not assigned |
| 24 V | 1, right | Down symply in my |
| 0 V | 5, right | Power supply input |
| N1 | 2, right | Come autouts |
| N2 | 6, right | Cam outputs |
| Latch | 3, right | Latch input |
| Gate | 7, right | Gate input |
| Ref | 4, right | Ref input |
| Shield | 8, right | Shield connection (DIN rail) |



3.2.3.3 Versions 750-637/000-002 and 750-637/000-004

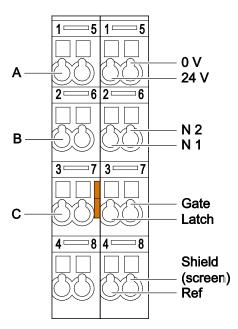


Figure 5: CAGE CLAMP® Connectors – Versions 750-637/000-002 and 750-637/000-004

Table 10: Legend for "CAGE CLAMP® Connectors – Versions $\,750\text{-}637/000\text{-}002$ and $\,750\text{-}637/000\text{-}004$ " Figure

| Designation | Connection | Function |
|-------------|------------|------------------------------|
| A | 1, left | |
| В | 2, left | Sensor connection |
| C | 3, left | |
| - | 4, left | |
| - | 5, left | |
| - | 6, left | Not assigned |
| - | 7, left |] |
| - | 8, left | |
| 24 V | 1, right | Davisa symply in myt |
| 0 V | 5, right | Power supply input |
| N1 | 2, right | Com outputs |
| N2 | 6, right | Cam outputs |
| Latch | 3, right | Latch input |
| Gate | 7, right | Gate input |
| Ref | 4, right | Ref input |
| Shield | 8, right | Shield connection (DIN rail) |

3.3 Display Elements

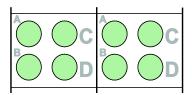


Figure 6: Display Elements

Table 11: Key for the "Display Elements" Figure, Versions 750-637 and 750-637/000-003

| Designation | LED | State | Function |
|-------------|-----------|-----------|---------------------------------------|
| A A, le | | Off | U(A) - (U(/A) < -0.2 V |
| | A, left | eft Green | U(A) - (U(/A) < 0.2 V or both inputs |
| | | Green | open |
| | | Off | U(B) - (U(/B) < -0.2 V |
| В | B, left | Cmaan | U(B) - (U(/B) < 0.2 V or both inputs |
| | | Green | open |
| | | Off | U(A) - (U(/A) < -0.2 V |
| C | C, left | Green | U(A) - (U(/A) < 0.2 V or both inputs |
| | | | open |
| Latch | D, left | Off | Input = 0 V or open |
| Laten | D, ICIt | Green | Input = 24 V |
| Coto | C-4- A 14 | Off | Input = 0 V or open |
| Gate | A, right | Green | Input = 24 V |
| NI1 | B, right | Off | Output = 0 V |
| N1 | | Green | Output = 24 V |
| Ref | C, right | Off | Input = 0 V or open |
| | | Green | Input = 24 V |
| N2 | D, right | Off | Output = 0 V |
| | | Green | Output = 24 V |



Table 12: Legend for the "Display Elements" Figure, Version 750-637/000-001

| Designation | LED | State | Function |
|-------------|----------|-------|----------------------------------------------------|
| A | A, left | Off | -30 V < (U(A) - U(/A)) < -15 V |
| | | Green | 30 V < (U(A) - U(/A)) < 15 V or both inputs open |
| B B, left | | Off | -30 V < (U(B) - U(/B)) < -15 V |
| | B, left | Green | 30 V < (U(B) - U(/B)) < 15 V or both inputs open |
| | | Off | -30 V < (U(C) - U(/C)) < -15 V |
| C C, | C, left | Green | 30 V < (U(C) - U(/C)) < 15 V or both inputs open |
| Latch | D, left | Off | Input = 0 V or open |
| Laten | | Green | Input = 24 V |
| C 4 | A, right | Off | Input = 0 V or open |
| Gate | | Green | Input = 24 V |
| N1 | B, right | Off | Output = 0 V |
| | | Green | Output = 24 V |
| Ref | C, right | Off | Input = 0 V or open |
| | | Green | Input = 24 V |
| N2 | D, right | Off | Output = 0 V |
| | | Green | Output = 24 V |

Table 13: Legend for the "Display Elements" Figure, Versions 750-637/000-002 and 750-637/000-004

| Designation | LED | State | Function |
|-------------|----------|-------|-----------------------------------|
| A | A, left | Off | 0 V < (U(A) < 5 V or input open |
| | | Green | 30 V > (U(B) > 15 V) |
| D | B, left | Off | 0 V < (U(B) < 5 V or input open |
| В | | Green | 30 V > (U(C) > 15 V) |
| C | 0.1.6 | Off | 0 V < (U(C) < 5 V or input open |
| C | C, left | Green | 30 V > (U(A) > 15 V) |
| Latale | D, left | Off | Input = 0 V or open |
| Latch | | Green | Input = 24 V |
| Cata | A, right | Off | Input = 0 V or open |
| Gate | | Green | Input = 24 V |
| NI1 | B, right | Off | Output = 0 V |
| N1 | | Green | Output = 24 V |
| Ref | C, right | Off | Input = 0 V or open |
| | | Green | Input = 24 V |
| N2 | D, right | Off | Output = 0 V |
| | | Green | Output = 24 V |



3.4 Operating Elements

The I/O module 750-637 has no operating elements.

3.5 Schematic Diagram

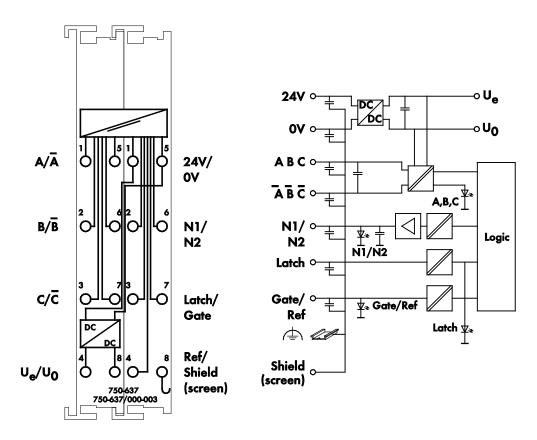


Figure 7: Schematic Diagram – Versions 750-637 and 750-637/000-003



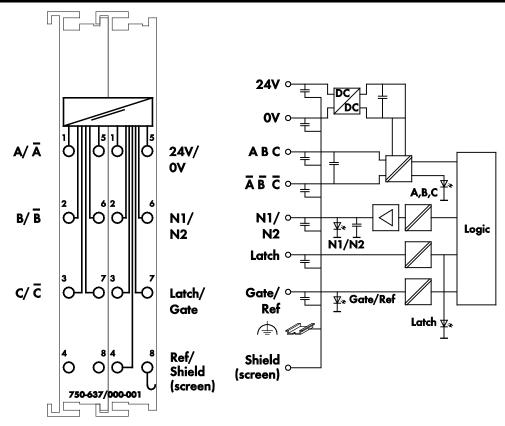


Figure 8: Schematic Diagram - Version 750-637/000-001

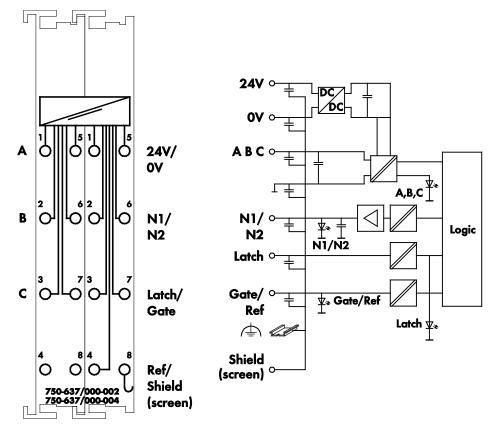


Figure 9: Schematic Diagram - Versions 750-637/000-002 and 750-637/000-004

3.6 Technical Data

3.6.1 Device Data

Table 14: Technical Data — Device

| Width | 24 mm |
|--------------------------------------|---------------|
| Height (from upper-edge of DIN rail) | 64 mm |
| Depth | 100 mm |
| Weight | approx. 105 g |
| Degree of protection | IP 20 |

3.6.2 Power Supply

Table 15: Technical Data — Power Supply

| 11.7 | Via CAGE CLAMP® connections |
|-----------------------------------------|------------------------------------|
| Power supply | 24 VDC (-15% +20%) |
| Current consumption max. (24 VDC) | 12 mA (without encoder or load) |
| Current consumption (internal) | 110 mA |
| Encoder output current _{max} . | 300 mA |
| Encoder operating voltage | 24 VDC |
| at 750-637 | 15 V/IVC |
| for 750-637/000-003 | |
| TT 1 10 11 11 | (0) 28.8 VDC > Supply voltage > |
| Undervoltage diagnostic | 20.4 VDC |
| for version 750-637/000-004 | (1) 0 VDC < Supply voltage < 5 VDC |

3.6.3 Communication

Table 16: Technical Data — Communication

| Data width, internal (internal data bus) | 1 × 32-bit data 2 × 8-bit control/status | |
|------------------------------------------|---------------------------------------------|--|



3.6.4 Inputs/Outputs

Table 17: Technical Data — Inputs/Outputs

| A /A D /D C /C |
|---------------------------------------------------|
| A, /A, B, /B, C, /C (/A, /B, /C are inverted)) |
| |
| A, B, C, 0 V |
| 24 VDC via field supply |
| 16 bits (increments per 1 ms) |
| See Table "Technical Data — |
| Quadrature Decoder" |
| 32 bits, binary |
| 32 bits |
| 32 bits |
| 32 bits |
| Reading, setting, activating |
| 250 kHz |
| 364 μs |
| 32 μs |
| 32 μs |
| |

3.6.5 Digital Outputs (N1, N2)

Table 18: Technical Data — Digital Outputs (N1, N2)

| Output voltage | 24 VDC |
|-----------------------------------|--------------------------------|
| Output current _{max.} | 0.5 A, short-circuit protected |
| Output resistance _{typ.} | 160 ΜΩ |

3.6.6 Digital Inputs (Latch, Gate, Ref)

Table 19: Technical Data — Digital Inputs (Latch, Gate, Ref), Versions 750-637, 750-637/000-003, 750-637/000-001

| Lagret violto oc | (0) -3 VDC +5 VDC |
|-------------------------------|---------------------|
| Input voltage | (1) +15 VDC +30 VDC |
| | Latch: 5 mA |
| Input current _{typ.} | Gate: 7 mA |
| | Ref: 7 mA |



3.6.7 Quadrature Inputs (A, /A, B, /B, C, /C)

Table 20: Technical Data — Quadrature Inputs (A, /A, B, /B, C, /C), Versions 750-637 and 750-637/000-003

| Input voltage A, /A | (0) U(A) - U(/A) < -0.2 VDC | |
|---------------------|-------------------------------------|--|
| | (1) U(A) - U(/A) > 0.2 VDC | |
| Input voltage B, /B | (0) U(B) - U(B) < -0.2 VDC | |
| | (1) $U(B) - U(B) > 0.2 \text{ VDC}$ | |
| Input voltage C, /C | (0) U(C) - U(/C) < -0.2 VDC | |
| | (1) U(C) - U(/C) > 0.2 VDC | |

Table 21: Technical Data — Quadrature Inputs (A, /A, B, /B, C, /C), Versions 750-637/000-002 and 750-637/000-004

| Input voltage A /A | (0) 0 VDC < U(A) < 5 VDC |
|-----------------------|------------------------------|
| Input voltage A, /A | (1) 30 VDC > U(A) > 15 VDC |
| Input valta as D. /D. | (0) 0 VDC < U(B) < 5 VDC |
| Input voltage B, /B | (1) 30 VDC > U(B) > 15 VDC |
| Investoral to a C /C | (0) 0 VDC < U(C) < 5 VDC |
| Input voltage C, /C | (1) 30 VDC > U(C) > 15 VDC |

Table 22: Technical Data — Quadrature Inputs (A, /A, B, /B, C, /C), Version 750-637/000-001

| Innut voltage A /A | (0) -30 VDC < U(A) - U(/A) < -15 VDC |
|----------------------|----------------------------------------|
| Input voltage A, /A | (1) +30 VDC > U(A) - U(/A) > +15 VDC |
| Luciat and to a D /D | (0) -30 VDC < U(B) - U(B) < -15 VDC |
| Input voltage B, /B | (1) +30 VDC > U(B) - U(B) > +15 VDC |
| Lucrot It C /C | (0) -30 VDC < U(C) - U(/C) < -15 VDC |
| Input voltage C, /C | (1) +30 VDC > U(C) - U(/C) > +15 VDC |



3.6.8 Quadrature Decoder

Table 23: Technical Data — Quadrature Decoder

| Table 23: Technical Data — Quadrature Decod | aer | | |
|---------------------------------------------|--------------------------------|--|--|
| Function | | | |
| 750-637 | | | |
| 750-637/000-001 | | | |
| 750-637/000-003 | Incremental Encoder Interfaces | | |
| 750-637/000-002 | | | |
| 750-637/000-004 | | | |
| Input | | | |
| 750-637 | IDC 400 | | |
| 750-637/000-003 | RS-422 | | |
| 750-637/000-001 | Differential | | |
| 750-637/000-002 | G: 1 1 1 | | |
| 750-637/000-004 | Single-ended | | |
| Encoder voltage | | | |
| 750-637 | 5 VDC | | |
| 750-637/000-003 | 5 VDC | | |
| 750-637/000-001 | | | |
| 750-637/000-002 | 24 VDC | | |
| 750-637/000-004 | | | |
| Evaluation | | | |
| 750-637 | | | |
| 750-637/000-001 | | | |
| 750-637/000-002 | 4× | | |
| 750-637/000-004 | | | |
| 750-637/000-003 | 1× | | |
| Data width | | | |
| 750-637 | | | |
| 750-637/000-001 | | | |
| 750-637/000-002 | 32 bits | | |
| 750-637/000-004 | | | |
| 750-637/000-003 | | | |
| | l . | | |



3.6.9 Climatic Environmental Conditions

Table 24: Technical Data – Climatic Environmental Conditions

| Tueste 2 :: Teethineur 2 utu ennium En (neimien) | car conditions |
|-------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------|
| Operating temperature range | 0 °C 55 °C |
| Storage temperature range | −25 °C +85 °C |
| Relative humidity without condensation | Max. 95 % |
| Resistance to harmful substances | Acc. to IEC 60068-2-42 and IEC 60068-2-43 |
| Maximum pollutant concentration at relative humidity < 75 % | $SO_2 \le 25 \text{ ppm}$ $H_2S \le 10 \text{ ppm}$ |
| Special conditions | Ensure that additional measures for components are taken, which are used in an environment involving: – dust, caustic vapors or gases – ionizing radiation |



3.7 Approvals

The following approvals have been granted to the basic version and all variants of 750-637 I/O modules:



Information

More information about approvals.

Detailed references to the approvals are listed in the document "Overview Approvals **WAGO-I/O-SYSTEM 750**", which you can find via the internet under: www.wago.com > SERVICES > DOWNLOADS > Additional documentation and information on automation products > WAGO-I/O-SYSTEM 750 > System Description.

CE

Conformity Marking



us cULus

UL508



Korea Certification

MSIP-REM-W43-DAM750

3.7.1 Ex Approvals

The following Ex approvals have been granted to the basic version and all variants of 750-637 I/O modules:

TÜV 07 ATEX 554086 X



I M2 Ex d I Mb II 3 G Ex nA IIC T4 Gc II 3 D Ex tc IIIC T135°C Dc

IECEX TUN 09.0001 X

Ex d I Mb Ex nA IIC T4 Gc Ex tc IIIC T135°C Dc



 $_{\rm C}UL_{\rm US}$

ANSI/ISA 12.12.01

Class I, Div2 ABCD T4



The following Ex approvals have been granted to the basic version of 750-637 I/O modules:

Brasilian-Ex Ex nA IIC T4 Gc

The following ship approvals have been granted to the basic version of 750-637 I/O modules:



ABS (American Bureau of Shipping)



Federal Maritime and Hydrographic Agency



BV (Bureau Veritas)



DNV (Det Norske Veritas) Class B



GL (Germanischer Lloyd) Cat. A, B, C, D (EMC 1)



KR (Korean Register of Shipping)



LR (Lloyd's Register) Env. 1, 2, 3, 4



NKK (Nippon Kaiji Kyokai)



PRS (Polski Rejestr Statków)



RINA (Registro Italiano Navale)



3.8 Standards and Guidelines

All variations of 750-637 I/O modules meet the following requirements on emission and immunity of interference:

EMC CE-Immunity to interference acc. to EN 61000-6-2

EMC CE-Emission of interference acc. to EN 61000-6-4

The I/O modules 750-637 meet the following requirements on emission and immunity of interference:

EMC marine applications-Immunity

to interference acc. to Germanischer Lloyd

EMC marine applications-Emission

of interference acc. to Germanischer Lloyd



4 Process Image



Note

Mapping of process data in the process image of fieldbus systems

The representation of the I/O modules' process data in the process image depends on the fieldbus coupler/controller used. Please take this information as well as the particular design of the respective control/status bytes from the section "Fieldbus Specific Design of the Process Data" included in the description concerning the process image of the fieldbus coupler/controller used.

The I/O module provides the fieldbus coupler/controller with a 6-byte input and output process image via 2 logical channels.

The set values to be set are stored in 4 output bytes (D0 ... D3). The read-in process data is stored in the 4 input bytes (D0 ... D3). Two control bytes (C0, C1) and two status bytes (S0, S1) control the data flow and are used to select the set and actual values.

Table 25: Process Image

| Process Image | | | | | | |
|---------------|---------------------------|----|------------------------|--|--|--|
| Input | | | Output | | | |
| S0 | Status byte S0 | C0 | Control byte C0 | | | |
| D0 | Process data byte 0 (LSB) | D0 | Set value byte 0 (LSB) | | | |
| D1 | Process data byte 1 | D1 | Set value byte 1 | | | |
| S1 | Status byte S1 | C1 | Control byte C1 | | | |
| D2 | Process data byte 2 | D2 | Set value byte 2 | | | |
| D3 | Process data byte 3 (MSB) | D3 | Set value byte 3 (MSB) | | | |

Various process data is displayed as a function of bit 0 and bit 1 in control byte C1 (see Table "MapPZD, All Versions, Except 750-637/000-004"):

- · Counter value
- Latch value
- Rate
- Set value

The setting is mirrored in bit 0 and bit 1 of status byte S1.



Table 26: MapPZD, All Versions, Except 750-637/000-004

| MapPZD (Control Byte C1/ Status Byte S1, Bit 0 and Bit 1) | | | | | |
|-----------------------------------------------------------|-------|-------------------------------------|--|--|--|
| Bit 1 | Bit 0 | Coding of Process Data | | | |
| 0 | 0 | Count value | | | |
| 0 | 1 | Latch value | | | |
| 1 | 0 | Rate in increments per milliseconds | | | |
| 1 | 1 | Set value | | | |

With Version 750-637/000-004, the following process data is displayed as a function of bit 0 in control byte C1 (see Table "MapPZD, Verstion 750-637/000-004"):

- · Counter value
- · Latch value

The setting is mirrored in bit 0 of status byte S1.

Table 27: MapPZD, Version 750-637/000-004

| MapPZD (Control Byte C1/ Status Byte S1, Bit 0) | | | |
|-------------------------------------------------|--------------------------|--|--|
| Bit 0 | O Coding of Process Data | | |
| 0 | Count value | | |
| 1 | Latch value | | |

4.1 Control/Status Byte

Table 28: Status Byte S0, All Versions, Except 750-637/000-004

| Status Byte S0, All Versions, Except 730-037/000-004 | | | | | | | | | |
|------------------------------------------------------|-----------|-------------------------------------------------------|-------------|-----------------------------------------------------------------------------------------------------|--------|-------|-------|-------|--|
| Bit 7 | Bit 7 Bit | | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 | |
| 0 | X | X AckSet | | OVER- | UNDER- | CNT- | LAT_ | LATC_ | |
| | | | LoadExt | FLOW | FLOW | SET_ | EXT_ | VAL | |
| | | | | | | ACK | VAL | | |
| | | Ack | Bit for EN- | N-LATC (C0.0) | | | | | |
| LATC_VAL | | Latch Mode: | | This bit is set on a positive edge at input C Reset is carried out when EN_LATC is reset | | | | | |
| | | Preload Mode: | | This bit is set on a positive edge at input C Reset is carried out when EN LATC is reset | | | | | |
| | | Ack | Bit for EN- | LAT_EXT (| C0.1) | | | | |
| LAT_EXT_VAL | | Latel | n Mode: | This bit is set on a positive edge at the Latch input Reset is carried out when EN LAT EXT is reset | | | | | |
| | | Prelo | ad Mode: | This bit is set on a positive edge at the Latch input Reset is carried out when EN LAT EXT is reset | | | | | |
| | | Ack Bit for CNT SET (C0.2) | | | | | | | |
| CNT-SET_A | ACK | Reset is carried out when CNT_SET=0. | | | | | | | |
| | | UNDERFLOW | | | | | | | |
| | | 1: On counter underflow from 0x00000000 to 0xffffffff | | | | | ffff | | |
| UNDERFLO | WC | 0: | | When counter value < 0xAAAAAAAA | | | | | |
| | | | | With a positive edge for ResetUnderflow (C0.3) | | | | | |
| | | | | When OVERFLOW=1 | | | | | |
| | | OVERFLOW | | | | | | | |
| | | 1: | | On counter overflow from 0xffffffff to 0x00000000 | | | | | |
| OVERFLO | W | | | When counter value < 0x55555555 | | | | | |
| | | 0: | | With a positive edge for ResetOverflow (C0.4) | | | | | |
| | | | When UND | ERFLOW=1 | | | | | |
| AckSet LoadExt | | Ack Bit for SetLoadExt | | | | | | | |
| | uLAt | ^1 - | | Reset is carried out when SetLoadExt=0 (C0.5) | | | | | |
| X | | Reserved | | | | | | | |
| 0 | | Reserved | | | | | | | |



Table 29: Status Byte S0, Version 750-637/000-004

| | | | | Status | Byte S0 | | | | | |
|-------------|-------|-------------------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------|----------------------------------------------------------|------------------------------|----------------|---------------|-------|--|--|
| Bit 7 | Bit | 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 | | |
| 0 | PI | 7 | AckSet | OUF | Enable_ | CNT- | LAT_ | LATC_ | | |
| | | | LoadExt | | Cam_ | SET_ | EXT_ | VAL | | |
| | | A -1- | D'4 C EN | LATO (CO. | Ack | ACK | VAL | | | |
| | | ACK | Ack Bit for EN-LATC (C0.0) This bit is set on a positive edge at input C | | | | | | | |
| LATC_VA | L | Late | h Mode: | Reset is carr | ried out when | EN_LATC | is reset | | | |
| | | Prelo | oad Mode: | | et on a positivited out when | | | | | |
| | | Ack | Bit for EN- | LAT_EXT (| (C0.1) | | | | | |
| LAT EXT | VAL | Late | h Mode: | | et on a positivited out when | | | t | | |
| | - | Prelo | oad Mode: | This bit is se | et on a positivited out when | ve edge at th | e Latch input | t | | |
| CN ITE CETE | . OT | Ack | Bit for CNT | | | | | | | |
| CNT-SET_ | ACK | _ | | | ried out when | CNT_SET= | =0. | | | |
| | | Ack | Bit for Ena | ble_Cam (C | 1.6) | _ | | | | |
| Enable_Car | n_Ack | _ | The command has been executed by the I/O module vack bit and the request bit are identical. The cam outputs have the desired state. | | | | le when the | | | |
| | | Combined Overflow/Underflow Status Bit | | | | | | | | |
| | | Underflow function ("counter-clockwise" rotation) | | | | | | | | |
| | | 1: On counter underflow from 0x00000000 to 0xffffffff | | | | | | ffff | | |
| | | | | When counter value is then < 0xAAAAAAAA | | | | | | |
| | | 0: | | On acknowledgement, i.e., positive edge RESET_OUF (C0.4) | | | | | | |
| OUF | | | | With an overflow event without previous acknowledgement | | | | | | |
| | | Ove | rflow function | n ("clockwis | se" direction | of rotation) | | | | |
| | | 1: | | On counter of | overflow from | n 0xffffffff t | o 0x0000000 |)0 | | |
| | | | | When counter value is then < 0x55555555 | | | | | | |
| | | 0: | | On acknowledgement, i.e., positive edge RESET_OUF (C0.4) | | | | | | |
| | | | | For underflow event without previous acknowledgement | | | | | | |
| AckSet Loa | dEvt | Ack | Bit for SetI | oadExt | | | | | | |
| ACKSCI LUA | uLAt | _ | | | ied out when | | | | | |
| | | Pow | erfail — A l | | l Power Sup | ply is Repor | rted | | | |
| | | 1: | | Field voltag | | | | | | |
| PF | | 0: | | | e is > 20.4 V | | | | | |
| | | | | | windows are | e set to 0. | | | | |
| | | | counter read | ing is set to (|). | | | | | |
| 0 | | Rese | Reserved | | | | | | | |



Table 30: Status Byte S1, All Versions, Except 750-637/000-004

| | Status Byte S1 | | | | | | | | | |
|---------------------|----------------|----------------------------|-----------------------------------------------------------------------------|-----------------------------|-------|--------------|--------|--|--|--|
| Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 | | | |
| 0 | 0 | N2 | N1 | StaN2 | StaN1 | MapPZD | MapPZD | | | |
| MapPZD | Cod | ing of Proce | ss Data | | | | | | | |
| MapPZD | Cod | ing of Proce | ss Data | | | | | | | |
| | Stat | us Cam Out | put 1 | | | | | | | |
| StaN1 | 0: | | Cam output | 1 is not set | | | | | | |
| | 1: | | Cam output 1 is set | | | | | | | |
| Status Cam Output 2 | | | | | | | | | | |
| StaN2 | 0: | 0: Cam output 2 is not set | | | | | | | | |
| | 1: | | Cam output | 2 is set | | | | | | |
| | Can | Window 1 | | | | | | | | |
| N1 | Bit i | s set when: | | arison value value N1_HI | | ounter value | ≤upper | | | |
| | Can | Cam Window 2 | | | | | | | | |
| N2 Bit | | s set when: | Lower comparison value N2_LO ≤ counter value ≤ upper comparison value N2_HI | | | | | | | |
| 0 | Rese | erved | | | | | | | | |
| 0 | Rese | Reserved | | | | | | | | |

Table 31: Status Byte S1, Version 750-637/000-004

| | | , | Status | Byte S1 | | | | | | |
|-----------|----------|-----------------|--------------------------------------------------------------------------------------------------------------------------------------------|---------------|----------------|-----------------|------------------------------|--|--|--|
| Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 | | | |
| 0 | 0 | N2_F1 | N1_F1 | N2_F2 | N1_F2 | Set Cam_ Ack | MapPZD | | | |
| MapPZD | C | oding of Proce | ess Data | | | | | | | |
| | C | ollective and A | Ack Bit for A | ll Cam Set I | Requests | | | | | |
| Set Cam_A | ek Bi | t is set when: | One or more byte. | request bists | s 2,3,4 or 5 a | re set by the | control | | | |
| | C | am 1, Window | <u>2</u> | | | | | | | |
| N1_F2 | | t is set when: | Lower comp comparison voltage pres | value N1F2_ | | | ue < upper 1.6) and field | | | |
| | C | Cam 2, Window 2 | | | | | | | | |
| N2_F2 | Bi | t is set when: | Lower comparison value N2F2_LO < counter value < upper comparison value N2F2_HI and cams enabled (C1.6) and field voltage present (S0.6=0) | | | | | | | |
| | C | Cam 1, Window 1 | | | | | | | | |
| N1_F1 | Bi | t is set when: | Lower comparison value N1F1_LO < counter value < upper comparison value N1F1_HI and cams enabled (C1.6) and field voltage present (S0.6=0) | | | | | | | |
| N2_F1 | | am 2, Window | 1 | | | | | | | |
| | | t is set when: | Lower comparison value N2F1_LO < counter value < upper comparison value N2F1_HI and cams enabled (C1.6) and fie voltage present (S0.6=0) | | | | | | | |
| 0 | R | eserved | | | | | | | | |
| 0 | Reserved | | | | | | | | | |



Table 32: Control Byte C0, All Versions, Except 750-637/000-004

| | | c co, All v cis. | | Byte C0 | | | | | |
|-------------------|-------|-----------------------------------------------------------------------------------------------------------------------------------------|------------------------|-------------------------|------------------------------|----------------|-------------|--|--|
| Bit 7 | Bit 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 | | |
| 0 | OpMod | e SetLoad Ext | RESET OVER- FLOW | RESET UNDER- FLOW | CNT_SET | EN_LAT_ EXT | EN_LATC | | |
| | Т | he initial poin | | | ed | | | | |
| | | Capture Mode: On a positive edge at input C, the counter value is trate to the Latch register. | | | | | | | |
| EN_LATC | Pı | reload Mode: | to the Latch | register. | out C, the counter | | transferred | | |
| | | onfirmation is ominant over E | | | ge at EN_LA | TC. EL_LA | TC is | | |
| | Т | The external Latch input is enabled | | | | | | | |
| | C | Capture Mode: On a positive edge at the LATCH input, the counter value is transferred to the Latch register. | | | | | | | |
| EN_LAT_E | | On a positive edge at the LATCH input, the counter value is transferred to the Latch register. The set value is loaded to the counter. | | | | | | | |
| | C | Confirmation is canceled on a negative edge at EN_LAT_EXT. | | | | | | | |
| CNT_SET | О | n a positive ed | lge, the coun | ter is initiali | ized to the so | et value | | | |
| RESET UNDERFLO | | he UNDERFL | OW status b | oit (S0.3) is r | eset on a po | sitive edge. | | | |
| RESET OVERFLO | | he OVERFLO |)W status bit | (S0.4) is res | set on a posi | tive edge. | | | |
| SetLoad Ex | | On a positive edge, the set value to which the counter is to be set for an external event is transferred to the process data. | | | | t for an | | | |
| | 0 | pMode | | - | | | | | |
| OpMode | 0: | | Capture Mo | de: The coun | ter is latched | l by a trigger | signal | | |
| Орглоце | 1: | | | | ter is latched o the counter | , | signal. The | | |
| 0 | R | eserved | | | | | | | |



Table 33: Control Byte C0, Version 750-637/000-004

| | | | ,, , , , , , , , , , , , , , , , , , , , | Control | Byte C0 | | | | | | |
|-----------------------------------------------------------------------------------------------------------------------------------------|-------|-------|------------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------|----------------------------------------------|----------------|----------------|-------------|--|--|--|
| Bit 7 | Bit 6 | | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 | | | |
| 0 | OpMoo | de | SetLoad Ext | RESET OUF | INV_DIR | CNT_SET | EN_LAT_ EXT | EN_LATC | | | |
| | Т | he i | nitial point | nitial point of the encoder is enabled | | | | | | | |
| | C | Captu | re Mode: | to the Latch | | | | | | | |
| EN_LATC | P | reloa | ad Mode: | to the Latch | e edge at inp register. e is loaded to | | | transferred | | | |
| | | | | | n is canceled s dominant o | | | N_LATC. | | | |
| | Т | he e | xternal La | tch input is o | enabled | | | | | | |
| | C | Captu | re Mode: | On a positive edge at the LATCH input, the counter | | | er value is | | | | |
| EN_LAT_E | EXT P | reloa | ad Mode: | On a positive edge at the LATCH input, the counter value is transferred to the Latch register. The set value is loaded to the counter. | | | | | | | |
| | | | | Confirmation is canceled on a negative edge at EN_LAT_EXT. | | | | | | | |
| CNT_SET | O |)n a | positive ed | ge, the coun | ter is initiali | zed to the so | et value | | | | |
| | II | NV_ | DIR | | | | | | | | |
| INV DIR | В | Bit = | 0: | The counting direction is positive, i.e., from left to right. | | | | | | | |
| IN V_DIK | В | it = | 1: | The counting | g direction is | negative, i.e | e., from right | to left. | | | |
| | N | TO | ICE: | | | | | | | | |
| RESET OU | F T | he (| OUF status | bit (S0.4) is | reset on a p | ositive edge. | · | | | | |
| SetLoad Ext On a positive edge, the set value to which the counter is to be set for external event is transferred to the process data. | | | | t for an | | | | | | | |
| | |)pM | | | • | | | | | | |
| OnMode | 0 | : | | Capture Mod | de: The coun | ter is latched | l by a trigger | signal | | | |
| OpMode | 1: | • | | Preload Mod | le: The count | ter is latched | by a trigger | | | | |
| 0 | R | Reser | ved | | | | | | | | |



Table 34: Control byte C1, All Versions, Except 750-637/000-004

| | | | | Control | Byte C1 | | | | | |
|-----------|----------|----------------|--------------------|------------------------------------------------------------------------------|---------------|--------------|-------------|----------|--|--|
| Bit 7 | Bit | 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 | | |
| 0 | Enable | e Ref | Set | Set | Dis | Dis | MapPZD | MapPZD | | |
| | <u> </u> | | Cam2 | Cam1 | Cam2 | Cam1 | | | | |
| MapPZD | | Codi | ng of Proce | ss Data | | | | | | |
| MapPZD | | Codi | ng of Proce | ss Data | | | | | | |
| | | Inhib | it Cam Ou | tput 1 | | | | | | |
| DisCam1 | | 0: | | Cam output | 1 enabled. | | | | | |
| | | 1: | | Cam output | 1 inhibited. | | | | | |
| | | Inhib | it Cam Ou | tput 2 | | | | | | |
| DisCam2 | | 0: | | Cam output | 2 enabled. | | | | | |
| | | 1: | | Cam output | 2 inhibited. | | | | | |
| | | Appl | Apply Cam Output 1 | | | | | | | |
| SetCam1 | | Positive edge: | | Set value applied as lower comparison value N1_LO for cam output 1 (N1). | | | | | | |
| | | Nega | tive edge | Set value applied as the upper comparison value N1_HI for cam output 1 (N1). | | | | | | |
| | | Appl | y Cam Out | | | | | | | |
| SetCam2 | = | Positi | ve edge: | Set value applied as lower comparison value N2_LO for cam output 2 (N2). | | | | | | |
| | = | Nega | tive edge | Set value applied as the upper comparison value N2_HI for cam output 2 (N2). | | | | | | |
| | | Enab | Enable REF Input | | | | | | | |
| | | | | The control | system sets t | he EnableRe | f bit | | | |
| | | | | The control | system must | enable EN_l | LATC (C0.0) | or or | | |
| EnableRef | | | | EN_LAT_EXT (C0.1) with a positive edge | | | | | | |
| | | | | A positive e | dge is genera | ted at input | REF | | | |
| | | | | | | | put C or LA | TCH will | | |
| | | | | result in a ca | pture or prel | oad event | | | | |
| 0 | | Rese | rved | | | | | | | |



Table 35: Control byte C1, Version 750-637/000-004

| 1 4010 35. 00 | Control Byte C1 Control Byte C1 | | | | | | | | |
|---------------|----------------------------------|------------------------------|-------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------------------|-----------------------------|----------------------------|----------------|--------------|--|
| Bit 7 | Bit | 6 | Bit 5 | Bit 4 | Bit 3 | Bit 2 | Bit 1 | Bit 0 | |
| 0 | Enab Car | | Set Cam2_ F1 | Set Cam1_ F1 | Set Cam2_ F2 | Set Cam1_ F2 | INV_ GATE | MapPZD | |
| | | Codi | ng of Proce | ss Data | | | | | |
| MapPZD | | 0: | | Counter value | | | | | |
| _ | | 1: | | Latch value | | | | | |
| | | Codi | ng of Proce | ss Data | | | | | |
| INV GATE | , | 0: | | The counting GATE input | | lisabled whe | n 24 V is ap | plied to the | |
| IIIV_GATE | | 1: | | | - 1 | disabled whe input is open | | lied to the | |
| | | | ICE: | | | wledged in th | ne status byte | e! | |
| | | Appl | y Cam Out | put 1, Wind | | | | | |
| SetNocke1_ | F2 | Posit | ive edge: | Set value applied as lower comparison value N1F2_LO for cam output 1 (N1) window F2 | | | | | |
| | | Nega | tive edge: | Set value applied as the upper comparison value N1F2_HI for cam output 1 (N1) window F2 | | | | | |
| | | Appl | y Cam Out | put 2, Wind | ow 2 | | | | |
| SetNocke2_ | F2 | Posit | ive edge: | Set value applied as lower comparison value N2F2_LO for cam output 2 (N2) window F2 | | | | | |
| | | Nega | Negative edge: Set value applied as the upper comparison value N2F2_cam output 2 (N2) window F2 | | | | | N2F2_HI for | |
| | | Apply Cam Output 1, Window 1 | | | | | | | |
| SetCam1_F | 1 | Posit | ive edge: | Set value applied as lower comparison value N1F1_LC cam output 1 (N1) window F1 | | | 1_LO for | | |
| | | Nega | tive edge | Set value applied as the upper comparison value N1F1_HI cam output 1 (N1) window F1 | | | | | |
| | | Appl | y Cam Out | put 2, Wind | ow 1 | | | | |
| SetCam2_F | 1 | Posit | ive edge: | Set value applied as lower comparison value N2F1_LO for cam output 2 (N2) window F1 | | | | | |
| | _ | Nega | tive edge | | plied as the up (N2) window | upper compar ow F1 | rison value N | N2F1_HI for | |
| | | Outp | out Voltage | | | | - | | |
| Enable Can | ne | 0: | | All cam outp | outs are inhib | oited, i.e., the | output volta | age is 0 V | |
| Litable_Call | 113 | 1: | | • | | bled, i.e., the | | age is 24 V | |
| | | | | knowledged | in the status | bit Enable_C | lams (S0.3). | | |
| 0 | | Rese | rved | | | | | | |



5 Function Description

As a rule, incremental encoders supply two output signals from the encoder tracks, offset by 90°. These signals are designated A and B. The difference between the input signals is evaluated in the incremental encoder I/O module.

In addition to the two track signals, incremental encoders normally also have an index track. This track contains only one impulse per rotation of the encoder. From this impulse and the counter value, the absolute position of the encoder can be determined within a revolution. As the index impulse is equal to the length of a line of the track signals, the latch process should always be carried out with the same direction of rotation.



Table 36: Function Description – Inputs

| Input | Description | I/O Module | Function | | | |
|----------|--------------------------------------------|-----------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--|--|--|
| | Quadrature inputs, | 750-637 | | | | |
| A, /A, | RS-422 | 750-637/000-003 | | | | |
| B, /B | Quadrature inputs, 24 V, differential | 750-637/000-001 | Track signal A or B of the incremental encoder | | | |
| A D | Quadrature inputs, | 750-637/000-004 | | | | |
| A,B | 24 V, single-ended | 750-637/000-002 | | | | |
| | Initial point input, | 750-637 | Track signal for the index | | | |
| C, /C | RS-422 | 750-637/000-003 | channel of the incremental | | | |
| C, 7C | Initial point input, 24 V, differential | 750-637/000-001 | encoder. Based on the operating | | | |
| | | 750-637/000-002 | mode, the content of the counter is placed in the | | | |
| С | Initial point input, single-ended | 750-637/000-004 | Latch register, or the set value also loaded to the counter on a positive edge at this input. | | | |
| | | 750-637 | Based on the operating | | | |
| | | 750-637/000-001 | mode, the content of the | | | |
| I A TOLL | LATCH input, | 750-637/000-002 | counter is placed in the | | | |
| LATCH | 24 V | 750-637/000-003 | Latch register, or the set value also loaded to the | | | |
| | | 750-637/000-004 | counter on a positive edge at this input. | | | |
| | | 750-637 | The counting process is disabled when 24 V is | | | |
| | | 750-637/000-001 | | | | |
| | | 750-637/000-002 | applied to the GATE input. | | | |
| | | 750-637/000-003 | applied to the GITTE input. | | | |
| GATE | GATE input, 24 V | 750-637/000-004 | The counting process is disabled when 24 V is applied to the GATE input. The counter is enabled when 0 V is present at the GATE input, or when the input is open. This allocation can be inverted by a control bit. | | | |
| | | 750-637 | During a reference run the | | | |
| | REF input, | 750-637/000-001 | index impulse is only | | | |
| DEE | 24 V | 750-637/000-002 | evaluated when an | | | |
| REF | | 750-637/000-003 | additional sensor signal (REF) is present. | | | |
| | REF input | 750-637/000-004 | The counter is set to 0 on a positive edge for REF | | | |
| | | 750-637 | Connection for shielding of | | | |
| Shield | Shielding | 750-637/000-001 | the encoder line. The shield (screen) is | | | |
| | | 750-637/000-002 | | | | |



Table 36: Function Description – Inputs

| Input | Description | I/O Module | Function | |
|-----------------------|----------------|-----------------|------------------------------------------------------------------------------------|--|
| | | 750-637/000-003 | directly connected to the | |
| | | 750-637/000-004 | DIN rail. | |
| | | 750-637 | | |
| | | 750-637/000-001 | 24.37 | |
| +24 V | Supply voltage | 750-637/000-002 | 24 V supply voltage for the I/O module | |
| | | 750-637/000-003 | | |
| | | 750-637/000-004 | | |
| | | 750-637 | Ground for the 24 V power | |
| | | 750-637/000-003 | supply and encoder signals connected internally with terminal V ₀ | |
| 0 V | Ground | 750-637/000-001 | Ground for 24 V supply voltage | |
| | | 750-637/000-002 | Ground for 24 V power | |
| | | 750-637/000-004 | supply and encoder signals | |
| V _e (+5 V) | Supply voltage | 750-637 | 5 V supply voltage for encoder | |
| $V_0 (0 V)$ | Ground | 750-637 | Ground for encoder signals and power supply connected internally with 0 V terminal | |

Table 37: Function Description — Outputs, All Versions, Except 750-637/000-004

| Output | Func | ction | |
|--------|------|---------------------------------------|--|
| N1, N2 | Cam | fam output, 24 V, 0.5 A | |
| | 0: | Counter value outside the set window. | |
| | 1: | Counter value within the set window. | |

Table 38: Function Description — Outputs, Version 750-637/000-004 Only

| Output | Function |
|--------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| N1, N2 | Cam output, 24 V, 0.5 A |
| | 0: Counter value outside the set window. |
| | 1: Counter value within the set window. |
| | Cams can be enabled and disabled globally. This guarantees that no unexpected switching states occur during ongoing operation due to changes of the windows. |
| | All cam outputs are reset (output votlage 0 V) on loss of field voltage. When voltage is restored, all cams remain reset, regardless of the previous switching state (output voltage 0 V). The loss of voltage is reported to the control unit via a status |
| | bit. The cam windows must then be set anew. |



6 Mounting

6.1 Mounting Sequence

Fieldbus couplers/controllers and I/O modules of the WAGO-I/O-SYSTEM 750/753 are snapped directly on a carrier rail in accordance with the European standard EN 50022 (DIN 35).

The reliable positioning and connection is made using a tongue and groove system. Due to the automatic locking, the individual devices are securely seated on the rail after installation.

Starting with the fieldbus coupler/controller, the I/O modules are mounted adjacent to each other according to the project design. Errors in the design of the node in terms of the potential groups (connection via the power contacts) are recognized, as the I/O modules with power contacts (blade contacts) cannot be linked to I/O modules with fewer power contacts.

△ CAUTION

Risk of injury due to sharp-edged blade contacts!

The blade contacts are sharp-edged. Handle the I/O module carefully to prevent injury.

NOTICE

Insert I/O modules only from the proper direction!

All I/O modules feature grooves for power jumper contacts on the right side. For some I/O modules, the grooves are closed on the top. Therefore, I/O modules featuring a power jumper contact on the left side cannot be snapped from the top. This mechanical coding helps to avoid configuration errors, which may destroy the I/O modules. Therefore, insert I/O modules only from the right and from the top.



Note

Don't forget the bus end module!

Always plug a bus end module 750-600 onto the end of the fieldbus node! You must always use a bus end module at all fieldbus nodes with WAGO-I/O-SYSTEM 750 fieldbus couplers/controllers to guarantee proper data transfer.



6.2 Inserting and Removing Devices

NOTICE

Perform work on devices only if they are de-energized!

Working on energized devices can damage them. Therefore, turn off the power supply before working on the devices.

6.2.1 Inserting the I/O Module

1. Position the I/O module so that the tongue and groove joints to the fieldbus coupler/controller or to the previous or possibly subsequent I/O module are engaged.



Figure 10: Insert I/O Module (Example)

2. Press the I/O module into the assembly until the I/O module snaps into the carrier rail.

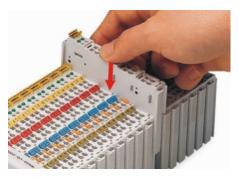


Figure 11: Snap the I/O Module into Place (Example)

With the I/O module snapped in place, the electrical connections for the data contacts and power jumper contacts (if any) to the fieldbus coupler/controller or to the previous or possibly subsequent I/O module are established.



6.2.2 Removing the I/O Module

1. Remove the I/O module from the assembly by pulling the release tab.

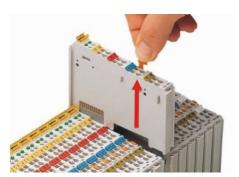


Figure 12: Removing the I/O Module (Example)

Electrical connections for data or power jumper contacts are disconnected when removing the I/O module.



7 Connect Devices



Note

Use shielded signal lines!

Only use shielded signal lines for analog signals and I/O modules which are equipped with shield clamps. Only then can you ensure that the accuracy and interference immunity specified for the respective I/O module can be achieved even in the presence of interference acting on the signal cable.

7.1 Connecting a Conductor to the CAGE CLAMP®

The WAGO CAGE CLAMP® connection is appropriate for solid, stranded and finely stranded conductors.



Note

Only connect one conductor to each CAGE CLAMP®!

Only one conductor may be connected to each CAGE CLAMP[®]. Do not connect more than one conductor at one single connection!

If more than one conductor must be routed to one connection, these must be connected in an up-circuit wiring assembly, for example using WAGO feed-through terminals.

Exception:

If it is unavoidable to jointly connect 2 conductors, then you must use a ferrule to join the wires together. The following ferrules can be used:

Length: 8 mm

Nominal cross section max.: 1 mm² for 2 conductors with 0.5 mm² each 216-103 or products with comparable properties

- 1. For opening the CAGE CLAMP® insert the actuating tool into the opening above the connection.
- 2. Insert the conductor into the corresponding connection opening.
- 3. For closing the CAGE CLAMP® simply remove the tool. The conductor is now clamped firmly in place.



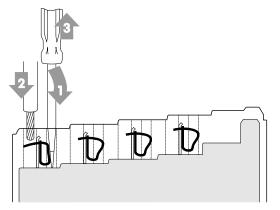


Figure 13: Connecting a Conductor to a CAGE CLAMP $^{\circledR}$



8 Use in Hazardous Environments

The **WAGO-I/O-SYSTEM 750** (electrical equipment) is designed for use in Zone 2 hazardous areas.

The following sections include both the general identification of components (devices) and the installation regulations to be observed. The individual subsections of the "Installation Regulations" section must be taken into account if the I/O module has the required approval or is subject to the range of application of the ATEX directive.



8.1 Marking Configuration Examples

8.1.1 Marking for Europe According to ATEX and IEC-Ex

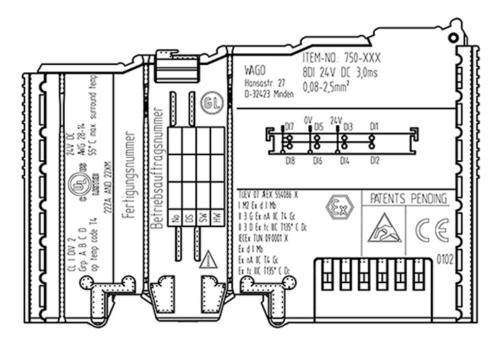


Figure 14: Side Marking Example for Approved I/O Modules According to ATEX and IECEx

TUEV 07 ATEX 554086 X

I M2 Ex d I Mb

II 3 G Ex na IIC T4 Gc

II 3 D Ex tc IIIC T135° C Dc

IECEX TUN 09.0001 X

Ex d I Mb

Ex na IIC T4 Gc

Ex tc IIIC T135° C Dc



Figure 15: Text Detail – Marking Example for Approved I/O Modules According to ATEX and IECEx.

Table 39: Description of Marking Example for Approved I/O Modules According to ATEX and IECEx

| Printing on Text | Description |
|-------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| TÜV 07 ATEX 554086 X | Approving authority and certificate numbers |
| IECEx TUN 09.0001 X | |
| Dust | |
| II | Equipment group: All except mining |
| 3D | Category 3 (Zone 22) |
| Ex | Explosion protection mark |
| tc Dc | Type of protection and equipment protection level (EPL):protection by enclosure |
| IIIC | Explosion group of dust |
| T 135°C | Max. surface temperature of the enclosure (without a dust layer) |
| Mining | |
| I | Equipment group: Mining |
| M2 | Category: High level of protection |
| Ex | Explosion protection mark |
| d Mb | Type of protection and equipment protection level (EPL): Flameproof enclosure |
| I | Explosion group for electrical equipment for mines susceptible to firedamp |
| Gases | |
| II | Equipment group: All except mining |
| 3G | Category 3 (Zone 2) |
| Ex | Explosion protection mark |
| nA Gc | Type of protection and equipment protection level (EPL): Non-sparking equipment |
| nC Gc | Type of protection and equipment protection level (EPL): Sparking apparatus with protected contacts. A device which is so constructed that the external atmosphere cannot gain access to the interior |
| IIC | Explosion group of gas and vapours |
| T4 | Temperature class: Max. surface temperature 135°C |



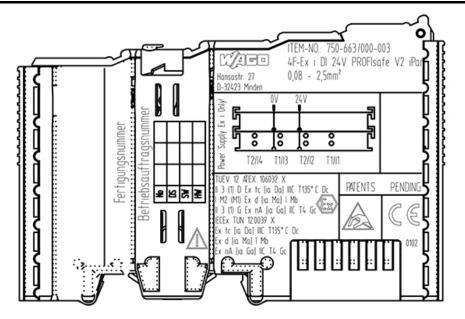


Figure 16: Side Marking Example for Approved Ex i I/O Modules According to ATEX and IECEx.

TUEV 12 ATEX 106032 X

II 3 (1) D Ex tc (ia Da) IIIC T135° C Dc

I M2 (M1) Ex d (ia Ma) I Mb

II 3 (1) G Ex nA (ia Ga) IIC T4 Gc

IECEX TUN 12.0039 X

Ex tc (ia Da) IIC T135° C Dc

Ex d (ia Ma) I Mb

Ex nA (ia Ga) IIC T4 Gc

Figure 17: Text Detail – Marking Example for Approved Ex i I/O Modules According to ATEX and IECEx.



Table 40: Description of Marking Example for Approved Ex i I/O Modules According to ATEX and IECEx

| Inscription Text | Description |
|-------------------------|----------------------------------------------------------------------------------|
| TÜV 07 ATEX 554086 X | Approving authority and certificate numbers |
| IECEx TUN 09.0001X | |
| TÜV 12 ATEX 106032 X | |
| IECEX TUN 12.0039 X | |
| Dust | |
| II | Equipment group: All except mining |
| 3(1)D | Category 3 (Zone 22) equipment containing a safety |
| | device for a category 1 (Zone 20) equipment |
| 3(2)D | Category 3 (Zone 22) equipment containing a safety |
| | device for a category 2 (Zone 21) equipment |
| Ex | Explosion protection mark |
| te De | Type of protection and equipment protection level (EPL): protection by enclosure |
| [ia Da] | Type of protection and equipment protection level |
| | (EPL): associated apparatus with intrinsic safety |
| | circuits for use in Zone 20 |
| [ib Db] | Type of protection and equipment protection level |
| | (EPL): associated apparatus with intrinsic safety |
| 777.6 | circuits for use in Zone 21 |
| IIIC | Explosion group of dust |
| T 135°C | Max. surface temperature of the enclosure (without a |
| Mining | dust layer) |
| T | Equipment Group: Mining |
| M2 (M1) | Category: High level of protection with electrical |
| INIZ (INII) | circuits which present a very high level of protection |
| Ex d Mb | Explosion protection mark with Type of protection |
| EX d WIO | and equipment protection level (EPL): Flameproof |
| | enclosure |
| [ia Ma] | Type of protection and equipment protection level |
| | (EPL): associated apparatus with intrinsic safety |
| | electrical circuits |
| I | Explosion group for electrical equipment for mines |
| | susceptible to firedamp |



Table 40: Description of Marking Example for Approved Ex i I/O Modules According to ATEX and $\overline{\text{IECEx}}$

| Gases | |
|---------|--------------------------------------------------------------------------------------------------------------------------------|
| II | Equipment group: All except mining |
| 3(1)G | Category 3 (Zone 2) equipment containing a safety device for a category 1 (Zone 0) equipment |
| 3(2)G | Category 3 (Zone 2) equipment containing a safety device for a category 2 (Zone 1) equipment |
| Ex | Explosion protection mark |
| nA Gc | Type of protection and equipment protection level (EPL): Non-sparking equipment |
| [ia Ga] | Type of protection and equipment protection level (EPL): associated apparatus with intrinsic safety circuits for use in Zone 0 |
| [ia Gb] | Type of protection and equipment protection level (EPL): associated apparatus with intrinsic safety circuits for use in Zone 1 |
| IIC | Explosion group of gas and vapours |
| T4 | Temperature class: Max. surface temperature 135°C |



8.1.2 Marking for America According to NEC 500

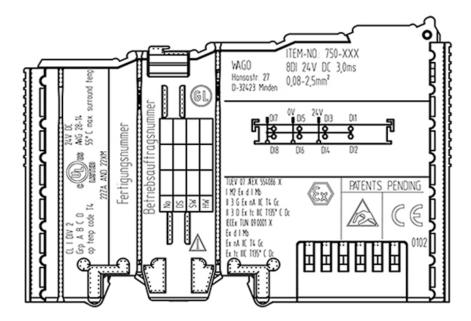


Figure 18: Side Marking Example for I/O Modules According to NEC 500



Figure 19: Text Detail – Marking Example for Approved I/O Modules According to NEC 500

Table 41: Description of Marking Example for Approved I/O Modules According to NEC 500

| Printing on Text | Description |
|-------------------------|----------------------------------------------|
| CL I | Explosion protection group (condition of use |
| | category) |
| DIV 2 | Area of application |
| Grp. ABCD | Explosion group (gas group) |
| Op temp code T4 | Temperature class |

8.2 Installation Regulations

For the installation and operation of electrical equipment in hazardous areas, the valid national and international rules and regulations which are applicable at the installation location must be carefully followed.



8.2.1 Special Conditions for Safe Use (ATEX Certificate TÜV 07 ATEX 554086 X)

- 1. For use as Gc- or Dc-apparatus (in zone 2 or 22) the Field bus Independent I/O Modules WAGO-I/O-SYSTEM 750-*** shall be erected in an enclosure that fulfils the requirements of the applicable standards (see the marking) EN 60079-0, EN 60079-11, EN 60079-15 and EN 60079-31. For use as group I electrical apparatus M2 the apparatus shall be erected in an enclosure that ensures a sufficient protection according to EN 60079-0 and EN 60079-1 and the degree of protection IP64. The compliance of these requirements and the correct installation into an enclosure or a control cabinet of the devices shall be certified by an ExNB.
- 2. Measures have to be taken outside of the device that the rating voltage is not being exceeded of more than 40 % because of transient disturbances.
- 3. Dip-switches, binary-switches and potentiometers, connected to the module may only be actuated when explosive atmosphere can be excluded.
- 4. The connecting and disconnecting of the non-intrinsically safe circuits is only permitted during installation, for maintenance or for repair purposes. The temporal coincidence of explosion hazardous atmosphere and installation, maintenance resp. repair purposes shall be excluded. This is although and in particular valid for the interfaces "Memory-Card", "USB", "Fieldbus connection", "Configuration and programming interface", "antenna socket", "D-Sub", "DVI-port" and the "Ethernet interface". These interfaces are not energy limited or intrinsically safe circuits. An operating of those circuits is in the behalf of the operator.
- 5. For the types 750-606, 750-625/000-001, 750-487/003-000, 750-484 and 750-633 the following shall be considered: The Interface circuits shall be limited to overvoltage category I/II/III (non mains/mains circuits) as defined in EN 60664-1.
- 6. For replaceable fuses the following shall be considered: Do not remove or replace the fuse when the apparatus is energized.
- 7. The following warnings shall be placed nearby the unit:
 WARNING DO NOT REMOVE OR REPLACE FUSE WHEN
 ENERGIZED
 WARNING DO NOT SEPARATE WHEN ENERGIZED
 WARNING SEPARATE ONLY IN A NON-HAZARDOUS AREA



8.2.2 Special Conditions for Safe Use (ATEX Certificate TÜV 12 ATEX 106032 X)

- 1. For use as Gc- or Dc-apparatus (in zone 2 or 22) the Field bus Independent I/O Modules WAGO-I/O-SYSTEM 750-*** Ex i shall be erected in an enclosure that fulfils the requirements of the applicable standards (see the marking) EN 60079-0, EN 60079-11, EN 60079-15 and EN 60079-31. For use as group I electrical apparatus M2 the apparatus shall be erected in an enclosure that ensures a sufficient protection according to EN 60079-0 and EN 60079-1 and the degree of protection IP64. The compliance of these requirements and the correct installation into an enclosure or a control cabinet of the devices shall be certified by an ExNB.
- 2. Measures have to be taken outside of the device that the rating voltage is not being exceeded of more than 40 % because of transient disturbances.
- 3. The connecting and disconnecting of the non-intrinsically safe circuits is only permitted during installation, for maintenance or for repair purposes. The temporal coincidence of explosion hazardous atmosphere and installation, maintenance resp. repair purposes shall be excluded.
- 4. For the type the following shall be considered: The Interface circuits shall be limited to overvoltage category I/II (non mains/mains circuits) as defined in EN 60664-1.



8.2.3 Special Conditions for Safe Use (IEC-Ex Certificate TUN 09.0001 X)

- 1. For use as Gc- or Dc-apparatus (in zone 2 or 22) the Field bus Independent I/O Modules WAGO-I/O-SYSTEM 750-*** shall be erected in an enclosure that fulfils the requirements of the applicable standards (see the marking) IEC 60079-0, IEC 60079-11, IEC 60079-15 and IEC 60079-31. For use as group I electrical apparatus M2 the apparatus shall be erected in an enclosure that ensures a sufficient protection according to IEC 60079-0 and IEC 60079-1 and the degree of protection IP64. The compliance of these requirements and the correct installation into an enclosure or a control cabinet of the devices shall be certified by an ExCB.
- 2. Measures have to be taken outside of the device that the rating voltage is not being exceeded of more than 40 % because of transient disturbances.
- 3. DIP-switches, binary-switches and potentiometers, connected to the module may only be actuated when explosive atmosphere can be excluded.
- 4. The connecting and disconnecting of the non-intrinsically safe circuits is only permitted during installation, for maintenance or for repair purposes. The temporal coincidence of explosion hazardous atmosphere and installation, maintenance resp. repair purposes shall be excluded. This is although and in particular valid for the interfaces "Memory-Card", "USB", "Fieldbus connection", "Configuration and programming interface", "antenna socket", "D-Sub", "DVI-port" and the "Ethernet interface". These interfaces are not energy limited or intrinsically safe circuits. An operating of those circuits is in the behalf of the operator.
- 5. For the types 750-606, 750-625/000-001, 750-487/003-000, 750-484 and 750-633 the following shall be considered: The Interface circuits shall be limited to overvoltage category I/II/III (non mains/mains circuits) as defined in IEC 60664-1.
- 6. For replaceable fuses the following shall be considered: Do not remove or replace the fuse when the apparatus is energized.
- 7. The following warnings shall be placed nearby the unit:
 WARNING DO NOT REMOVE OR REPLACE FUSE WHEN
 ENERGIZED
 WARNING DO NOT SEPARATE WHEN ENERGIZED
 WARNING SEPARATE ONLY IN A NON-HAZARDOUS AREA



8.2.4 Special Conditions for Safe Use (IEC-Ex Certificate IECEx TUN 12.0039 X)

- 1. For use as Gc- or Dc-apparatus (in zone 2 or 22) the Field bus independent I/O Modules WAGO-I/O-SYSTEM 750-*** Ex i shall be erected in an enclosure that fulfils the requirements of the applicable standards (see the marking) IEC 60079-0, IEC 60079-11, IEC 60079-15, IEC 60079-31. For use as group I electrical apparatus M2 the apparatus shall be erected in an enclosure that ensures a sufficient protection according to IEC 60079-0 and IEC 60079-1 and the degree of protection IP64. The compliance of these requirements and the correct installation into an enclosure or a control cabinet of the devices shall be certified by an ExCB.
- 2. Measures have to be taken outside of the device that the rating voltage is not being exceeded of more than 40 % because of transient disturbances.
- 3. The connecting and disconnecting of the non-intrinsically safe circuits is only permitted during installation, for maintenance or for repair purposes. The temporal coincidence of explosion hazardous atmosphere and installation, maintenance resp. repair purposes shall be excluded.
- 4. For the type the following shall be considered: The Interface circuits shall be limited to overvoltage category I/II (non mains/mains circuits) as defined in IEC 60664-1.



8.2.5 Special Conditions for Safe Use According to ANSI/ISA 12.12.01

- A. "This equipment is suitable for use in Class I, Division 2, Groups A, B, C, D or non-hazardous locations only."
- B. "This equipment is to be fitted within tool-secured enclosures only."
- C. "WARNING Explosion hazard substitution of components may impair suitability for Class I, Div. 2."
- D. "WARNING Do not disconnect equipment unless power has been switched off or the area is known to be non-hazardous" has to be placed near each operator accessible connector and fuse holder.
- E. When a fuse is provided, the following information shall be provided: "A switch suitable for the location where the equipment is installed shall be provided to remove the power from the fuse."
- F. For devices with EtherCAT/Ethernet connectors "Only for use in LAN, not for connection to telecommunication circuits."
- G. "WARNING Use Module 750-642 only with antenna module 758-910."
- H. For Couplers/Controllers and Economy bus modules only: The instructions shall contain the following: "The configuration interface Service connector is for temporary connection only. Do not connect or disconnect unless the area is known to be non-hazardous. Connection or disconnection in an explosive atmosphere could result in an explosion."
- I. Modules containing fuses only: "WARNING Devices containing fuses must not be fitted into circuits subject to over loads, e.g. motor circuits."
- J. Modules containing SD card reader sockets only: "WARNING Do not connect or disconnect SD-Card while circuit is live unless the area is known to be free of ignitable concentrations of flammable gases or vapors."



Information

Additional Information

Proof of certification is available on request.

Also take note of the information given on the operating and assembly instructions.

The manual, containing these special conditions for safe use, must be readily available to the user.



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